



राष्ट्रीय प्रौद्योगिकी संस्थान सिलचर National Institute of Technology Silchar

(राष्ट्रीय महत्व का संस्थान)

(An Institute of National Importance)

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Advertisement No.: NITS/Dean(RC)/ECE/541/02/2021

Date: 09.09.2021

Applications are invited from Indian citizens for temporary post of **Junior Research Fellow (JRF)** for the **CSIR EMR-II** sponsored project in the Department of Electronics & Communication Engineering, NIT Silchar.

Title of the project	High Pressure Phase Transitions, Electronic, Elastic and Optical Properties of Selected Defect Chalcopyrite Semiconductors for photodetector Application
Sponsored Agency	Council of Scientific and Industrial Research (CSIR) EMR-II
Project sanction number	CSIR Project no. 70(0076)/19/EMR-II
No. of posts	01
Duration	01 year or extendable till the completion of the project.
Principal Investigator (P.I)	Dr. Susanta Kumar Tripathy

General Information and Terms & Conditions

1.	Age Limit	28 Years
2.	Essential Qualification	M.E / M.Tech in Electronics and Communication Engineering or Equivalent degree along with GATE/NET qualification. OR M.Sc in Electronics/Physics or equivalent degree with GATE/NET qualification.
3.	Desirable Qualification	Expertise and strong interests in experimental Solar Cell Fabrication/nanotechnology/sensors/materials science/ materials chemistry/ solid-state physics/density functional theory. <i>Throughout First Class in academic career is preferred.</i>
4.	Salary	Rs. 31,000/- per month
5.	Relaxation on qualification and age	SC/ST/OBC/EWS/WOMEN/PWD candidates will get relaxation as per Govt of India Rules.
6.	Last date of receiving/ submission of filled up application form	24.09.2021
7.	Reporting	Applicants are requested to submit the signed scanned copy of their filled-up applications along with C.V and all other relevant documents to Dr. S. K. Tripathy, PI, Department of ECE, NIT Silchar, Assam-788010 through email: sktripathy@ece.nits.ac.in (+919465407913) with subject line " Application for the post of JRF under the CSIR project " on or before 24.09.2021 (24 th September, 2021) up to 5 PM IST. Application received after the last date will not be accepted.
8.	Date of interview, time, and venue	01.10.2021, 11.00 AM, Department of Electronics & Communication Engineering, NIT Silchar. If pandemic conditions and Government regulations prevailed at the time of interview, it will be conducted through online mode, which will be notified in due course of time.
9.	No objection certificate	Employed candidates are to submit no objection certificate from his / her employer at the time of interview.
10.	Institute right	The Institute has the discretion to restrict the number of candidates to be called for the interview on the basis of screening. No candidate may be recruited if suitable candidates are not found.
11.	Signing of the agreement	Selected candidates shall have to sign an agreement at the time of joining.

Sd/-

Associate Dean (R&C)
NIT Silchar.

14. NET/GATE Examinations Passed:

I. NET

II. GATE

Subject	Qualifying date	Valid upto		Subject	Score	Valid upto

15. Details of employments:

Sl. No.	Name of the Organization	Name of the post	Period	Scale of pay and present pay	Nature of duty

16. Any other relevant information that you may like to furnish:

Declaration

I declare that the above information is true and correct to the best of my knowledge and belief.

Date:

Place

Signature of the applicant