



राष्ट्रीय प्रौद्योगिकी संस्थान सिलचर National Institute of Technology Silchar

(राष्ट्रीय महत्व का संस्थान)

(An Institute of National Importance)

www.nits.ac.in, Phone: 03842-242914, Fax: 03842-224797, Assam-788010 (INDIA)

Advertisement No. : NITS/Dean(R&C)/EE/549/1/2020

Date: 17/01/2020

Applications are invited from Indian citizens for a temporary of post of **Junior Research Fellow (JRF)** for the **SERB** sponsored project in the Department of Electrical Engineering, NIT Silchar.

Title of the project	Design and development of soft interconnect for DC Microgrids using power electronics interface for improved reliability of power supply
Sponsored Agency	SERB
Project sanction number	SRG/2019/001042
No. of posts	01 for JRF
Duration	2 years
Principal Investigator (P.I)	Dr. Amrithesh Kumar, Department of Electrical Engineering, National Institute of Technology Silchar, Silchar- 788010, India. Email: amrithesh.nits@gmail.com

General Information and Terms & Conditions

1.	Age Limit	35 Years
2.	Essential Qualification	M.Tech/M.E. in Electrical Engineering (Specialization- Power Electronics). Candidates must be GATE/NET qualified (including lectureship)
3.	Desirable Experience	Candidates should have hands on experience in developing and working on hardware / laboratory prototype of the Power electronics converters, PCB design software besides knowledge of microcontroller/DSP.
4.	Salary	Rs. 31,000/- per month
5.	Relaxation on qualification and age	SC/ST/OBC/WOMEN/PWD candidates will get relaxation as per Govt of India Rules.
6.	Last date of receiving/submission of filled up application form	07/02/2020
7.	Reporting	Applicants are requested to submit their filled-up application along with C.V. to the P.I. accompanying photocopies of all documents. Applicants are also requested to send the soft copy of the duly filled application along with C.V. & all necessary documents to the P.I through email (amrithesh.nits@gmail.com). Call letter for shortlisted candidates will be sent through email.
8.	Probable date of interview, time, and venue	20/02/2020 , 12 Noon, Seminar Hall, Electrical Engineering department
9.	No objection certificate	Employed candidates are to submit no objection certificate from his / her employer at the time of interview.
10.	Institute right	The Institute has the discretion to restrict the number of candidates to be called for the interview on the basis of screening. No candidate may be recruited if suitable candidates are not found.
11.	Signing of the agreement	Selected candidates shall have to sign an agreement at the time of joining.

Sd/-

Associate Dean (R&C)
NIT Silchar.

National Institute of Technology Silchar

FORMAT of APPLICATION

for **Junior Research Fellow**

Paste recent
passport size
colored
photograph

1. Advertisement no. dated:
2. Title of the Project-
.....
3. Department:
4. Name of the applicant (in block letters):
5. Father's/Husband's Name:
6. Date of Birth (DD-MM-YYYY):
7. Age as on 07-02-2020:
8. Postal Address:

E-mail: Phone no.:

9. Permanent Address:
10. Nationality:
11. Marital Status:
12. Whether belongs to SC/ST/PH/OBC: (If yes, attach certificate):
13. Educational Qualification: (Starting from Class X)

Exam. passed	Board/ University	Division	% age of marks / CPI	Year of passing	Subjects

14. NET/GATE Examinations Passed:

I. NET

II. GATE

Subject	Qualifying date	Valid upto		Subject	Score	Valid upto

15. Details of employments:

Sl. No.	Name of the Organization	Name of the post	Period	Scale of pay and present pay	Nature of duty

16. Any other relevant information that you may like to furnish:

Declaration

I declare that the above information are true and correct to the best of my knowledge and belief.

Date:

Place

Signature of the applicant