



Invitation Letter

National Institute of Technology Silchar

Department of Electronics & Communication Engineering

Organizes 5-Days Workshop on

**Emerging Nanoscale Semiconductor
Devices and integration with Machine
Learning Verification**



**Under the aegis of New Education
Policy 2020, Govt. of India**

14th to 18th May, 2025

Chief-Patron

Prof. Dilip Kumar Baidya
Director, NIT Silchar

Patron

Dr. Koushik Guha
Head, ECE Department, NIT Silchar

Advisor

Prof. F. A. Talukdar, Professor
Department of ECE, NIT Silchar

Prof. Srimanta Baishya, Professor
Department of ECE, NIT Silchar

Convener

Prof. Brinda Bhowmick
Dean Academic, NIT Silchar

Coordinators

Dr. Arun Kumar
Assistant Professor, ECE Department
NIT Silchar

Dr. Rajesh Saha
Assistant Professor, ECE Department
NIT Silchar

Dr. Dipjyoti Das
Assistant Professor, ECE Department
NIT Silchar

Dr. Lachit Dutta
Assistant Professor, ECE Department
Gauhati University

SPEAKERS

- Prof. Hafizur Rahaman, Professor, IEST Shibpur
- Prof. Sourav Sarkar, Professor, Jadavpur University
- Dr. C. Periasamy, Associate Professor, NIT Calicut
- Dr. Ankush Bag, Associate Professor, IIT Guwahati
- Dr. Shubham Sahay, Associate Professor, IIT Kanpur
- Dr. R. Goswami, Assistant Professor, Tezpur University
- Dr. Lachit Dutta, Assistant Professor, Gauhati University
- Dr. Sumit Kumar, Assistant Professor, Gati Shakti Vishwavidyalaya
- Dr. Satyam Shukla, Design Engineer, ST Microelectronics
- Dr. Pardeep Duhan, Assistant Professor, IIT Ropar
- Dr. Nirmal Ch Roy, Assistant professor, MNNIT Allahabad