5-Days Hybrid Workshop on

Emerging Nanoscale Semiconductor Devices and Integration with Machine Learning Verification

Under the aegis of New Education Policy 2020, Govt. of India

Organized by
Department of Electronics and Communication Engineering, NIT Silchar
Department of Electronics and Communication Engineering, Gauhati University

Eligibility/Target Audience

PhD Scholars/ UG/ PG Students/ Faculties of AICTE/ UGC affiliated institutes.

Applications will be accepted on First cum first serve basis.

E-Certificates will be issued to the participants only after attending the complete workshop.

No Fee Required for Registration/ Participation

REGISTER in Below Link
https://forms.gle/nAWVg6iaqRcfw3N57
Last Date Registration: 10 May, 2025

14th – 18th May 2025

About NEP

The New Education Policy (NEP) of India, announced in 2020, aims to transform the country's education system by making it more inclusive, flexible, and aligned with the needs of the 21st century. The major highlights of NEP 2020 are: mother tongue or regional language as medium of instruction; promotes interdisciplinary learning; focus on critical thinking, problem-solving, and application-based learning; expanding digital learning tools and platforms; implementation of a National Research Foundation to enhance research quality in India; and encourages sustainable practices and global collaboration in education.

About the Workshop

Five days hybrid workshop from 14th-18th May, 2025 is being organised by **Department of Electronics and Communication engineering, National Institute of Technology Silchar, and Department of Electronics and Communication Engineering, Gauhati University, under NEP 2020, Govt. of India**. NIT Silchar is ranked 40th in NIRF 2024 among Top Engineering Institutions in India. This workshop will provide an opportunity to learn recent trends and challenges in the area of CMOS Technologies and its beyond from eminent experts from IITs/NITs/Industry. Also, the importance of NEP and its benefits/challenges to be discussed by our experts. The participants will have an exposure to the state-of-the-art in CMOS IC Design and challenges in advanced semiconductor devices for various applications and learn new IC simulation Tools e.g. EDA/ TCAD tools from academic/industry experts.

About the Department of ECE, NIT Silchar

The Department of Electronics & Communication Engineering offers a four-year B. Tech. program and two-year M. Tech. programs in "Microelectronics and VLSI Design", "Communication and Signal Processing Engineering", "RF and Terahertz Communications", and Ph.D. in various areas of the discipline. Two PG Programs (M. Tech. in ME&VLSID and M. Tech. in CSPE) are NBA accredited. The faculties of the department have a wide variety of expertise with a strong academic background and are alumni of reputed institutes around the world. To complement this, the department has well-stocked laboratories housing various modern pieces of equipment to enable the students to keep pace with the fast-changing technology. The various specializations like Artificial Intelligence, Device modelling, MEMS devices, optoelectronics, Antenna, RF-microwave, Wireless Networks, Image Processing, etc.

Esteemed Speakers (Tentative)

- Prof. Hafizur Rahaman, Professor, IIEST Shibpur
- Dr. C. Periasamy, Associate Professor, NIT Calicut
- Dr. Ankush Bag, Associate Professor, IIT Guwahati
- Dr. R. Goswami, Assistant Professor, Tezpur University
- Dr. L. Dutta, Assistant Professor, Gauhati University
- Dr. Sumit Kumar, Assistant Professor, Gati Shakti Vishwavidyalaya
- Dr. Satyam Shukla, Design Engineer, ST Microelectronics
- Dr. Pardeep Duhan, Assistant Professor, IIT Ropar
- Dr. Nirmal Ch Roy, Assistant professor, MNNIT Allahabad

Organizing Team

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Prof. Dilip Kumar Baidya,
Director, NIT Silchar

Patron
Dr. Koushik Guha
Associate Professor & Head,
ECE Department, NIT Silchar

Advisor
Prof. F. A. Talukdar,
Professor
ECE Department, NIT Silchar

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